

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Taylor R. Efland
Serial No.: 09/975,630
Filed: 10/12/01
For:

Art Unit: 2826
Examiner: Leonardo Andujar
Docket: TI-31678

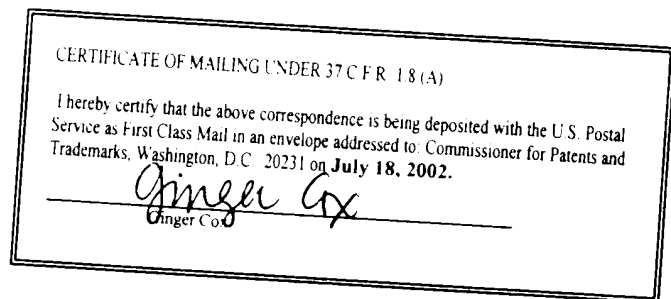
CIRCUIT STRUCTURE INTEGRATING THE POWER DISTRIBUTION
FUNCTIONS OF CIRCUITS AND LEADFRAMES INTO THE CHIP
SURFACE

513
Election
J. McNeill
8/9/02

ELECTION

July 18, 2002

Commissioner of Patents
and Trademarks
Washington, D.C. 20231



Sir:

Responsive to the Office Action dated June 28, 2002, please enter the following:
For prosecution, Applicant elects Group I, Claims 1-19, drawn to a semiconductor device, classified in class 257, subclass 691.

Applicant acknowledges that Claims 20-23 are withdrawn from further examination.
A prompt action on the merits is respectfully requested.

Respectfully submitted,

Gary C. Honeycutt

Gary C. Honeycutt

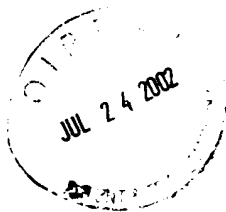
Reg. No. 20,250
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2826



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July 18, 2002

VIA FIRST CLASS MAIL

Commissioner for Patents
and Trademarks
Washington, D.C. 20231

Re: Patent Application for:
**"Circuit Structure Integrating the Power Distribution Functions of Circuits
and Leadframes into the Chip Surface"**
Serial No.: 09/975,630
Attorney Docket No.: TI-31678
Our File: 50000.2156


Dear Sir:

Enclosed for filing please find the following items relating to the above-identified application:

- (1) Election Document; and
- (2) Postcard.

Please file the above documents and return the date-stamped postcards to the corresponding addresses as indicated. In the meantime, if you have any questions or comments concerning this matter, please call the undersigned. Otherwise, please accept the enclosed.

Sincerely,


Gary C. Honeycutt
Reg. No. 20,250

GCH:gle
Enclosures

